•	Application No.	Application No. Applicant(s)		
Notice of Allowability	09/536,993	SUEHIRO, MITSUO	SUEHIRO, MITSUO	
	Examiner	Art Unit		
	Ishwar (I. B.) Patel	2827		
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.31:	(OR REMAINS) CLOSED) or other appropriate comr RIGHTS. This application is	in this application. If not includ nunication will be mailed in due	ed course. THIS	
<ol> <li>This communication is responsive to response filed on Ma</li> <li>The allowed claim(s) is/are 8 and 17.</li> <li>The drawings filed on 30 September 2002 are accepted by</li> <li>Acknowledgment is made of a claim for foreign priority until a) All b) Some* c) None of the:         <ol> <li>Certified copies of the priority documents have</li> <li>Certified copies of the priority documents have</li> <li>Copies of the certified copies of the priority documents have</li> </ol> </li> </ol>	y the Examiner. der 35 U.S.C. § 119(a)-(d) e been received. e been received in Applicat	ion No	ation from the	
* Certified copies not received:  5. Acknowledgment is made of a claim for domestic priority u  (a) The translation of the foreign language provisional a  6. Acknowledgment is made of a claim for domestic priority u  Applicant has THREE MONTHS FROM THE "MAILING DATE" of below. Failure to timely comply will result in ABANDONMENT of	application has been receivender 35 U.S.C. §§ 120 and of this communication to file	red.  i/or 121.  a reply complying with the requ		
7. A SUBSTITUTE OATH OR DECLARATION must be subrINFORMAL PATENT APPLICATION (PTO-152) which gives rea			NOTICE OF	
<ul> <li>8.  CORRECTED DRAWINGS must be submitted.</li> <li>(a)  including changes required by the Notice of Draftsper 1)  hereto or 2)  to Paper No</li> <li>(b)  including changes required by the proposed drawing (c)  including changes required by the attached Examiner</li> </ul>	correction filed, wh	nich has been approved by the l		
Identifying indicia such as the application number (see 37 CFR 1 each sheet.	l.84(c)) should be written on	the drawings in the front (not the	a back) of	
9. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT FOR 1			Note the	
Attachment(s)				
<ul> <li>1 Notice of References Cited (PTO-892)</li> <li>3 Notice of Draftperson's Patent Drawing Review (PTO-948)</li> <li>5 Information Disclosure Statements (PTO-1449), Paper No</li> <li>7 Examiner's Comment Regarding Requirement for Deposit of Biological Material</li> </ul>	4∏ Intervie 6⊠ Exami	of Informal Patent Application ( ew Summary (PTO-413), Paper ner's Amendment/Comment ner's Statement of Reasons for	No	

## **EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Thomas E. Brown (44,450) on June 18, 2003.

The application has been amended as follows:

- (a) In claim 8, delete "so as to fix" and add - connecting -, line 4-5, and delete "for containing the solder bump", line 7.
  - (b) Cancel claims 10-15, not elected without traverse.
- 2. The following is an examiner's statement of reasons for allowance:

A printed circuit board unit connecting a printed circuit board with an electronic component with an interposer disposed in between with a though hole in the interposer,

wherein the through hole with coating on internal wall in contact with the solder bump, as shown by Tsunoi, US Patent No. 6,429,516 or

through hole without coating, having internal surface of the through hole in the interposer not touching the solder bump, as shown by Ellerson et al., US Patent No. 5,859,470, are known.

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However, printed circuit board unit, with the through hole in the interposer coated with material wet to solder, but not contacting the solder bump, spaced away from the bump, when assembled, as shown in figure 18 and enabled by the disclosure, has not been taught or disclosed by the prior art.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ishwar (I. B.) Patel whose telephone number is (703) 305 2617. The examiner can normally be reached on M-F (8:30 - 5).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David L Talbott can be reached on (703) 305 9883. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 305 3431 for regular communications and (703) 305 7724 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308 0956.

KAMAND CUNEO
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2860

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